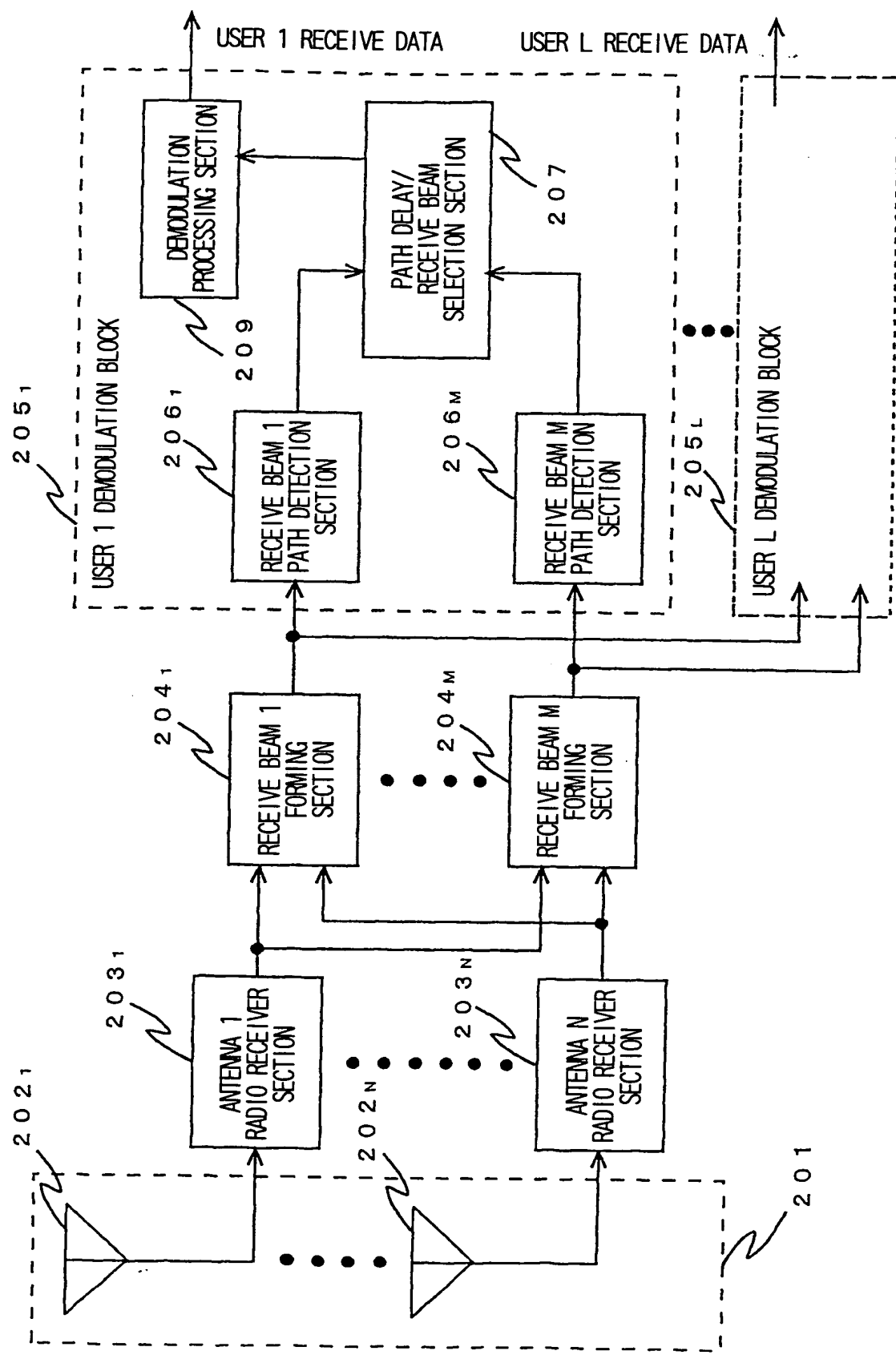
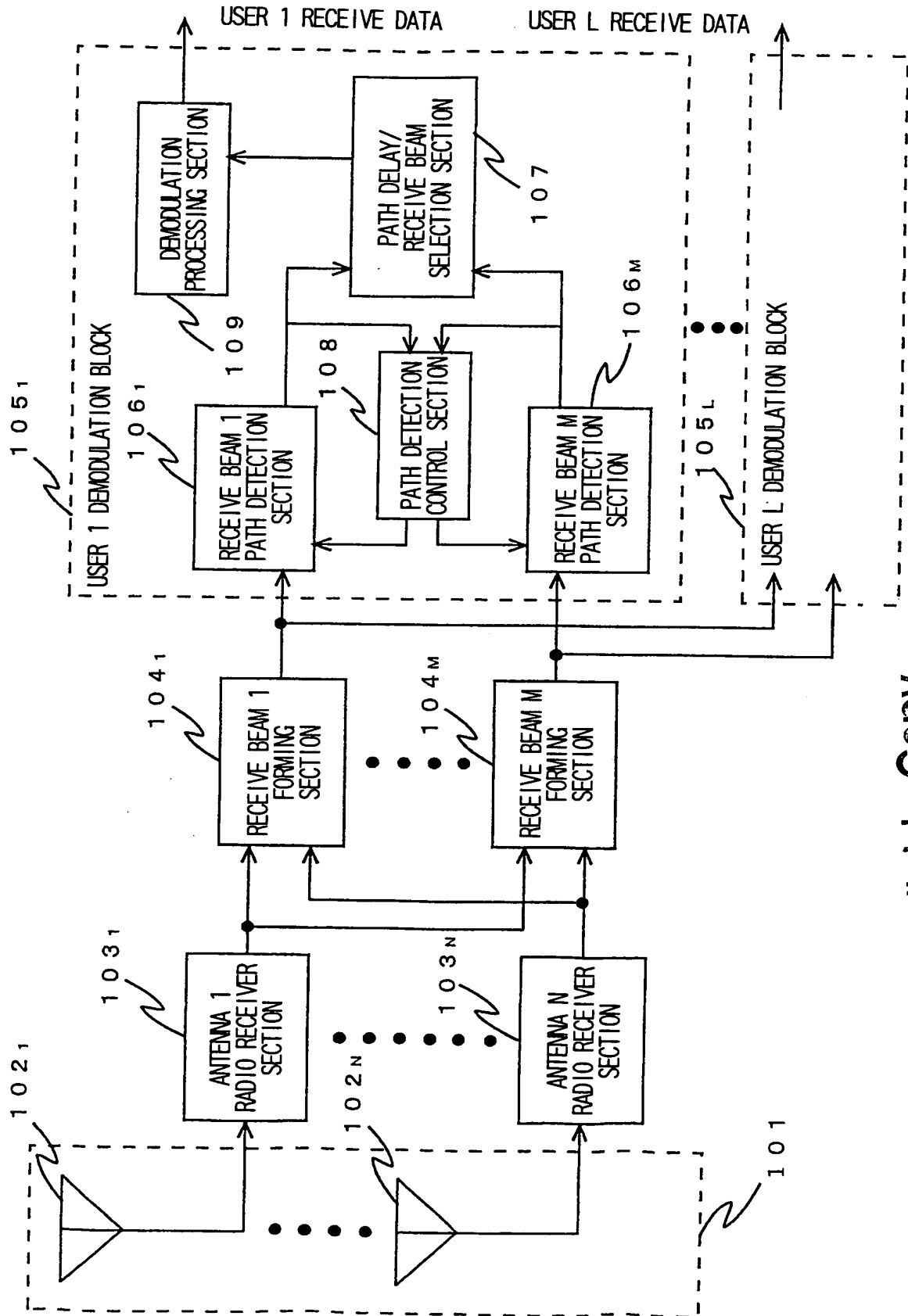


FIG. 1



Rest Available Copy

FIG. 2



Rest Available Copy

# F I G. 3

PATH DELAY (UNIT: CHIP)	RECEIVE BEAM NUMBER (PEAK ANGLE)											
	0	1	2	3	4	5	6	7	8	9	10	11
	(-55)	(-45)	(-35)	(-25)	(-15)	(-05)	(+05)	(+15)	(+25)	(+35)	(+45)	(+55)
0.0												
0.5												
1.0												
1.5												
2.0												
2.5												
3.0												
3.5												
4.0												
4.5												
5.0												
5.5												
6.0												
6.5												
7.0												
7.5												
8.0												
8.5												
9.0												
9.5												
10.0												
10.5												



PAIRS OF RECEIVE BEAM NUMBERS AND PATH DELAYS DETECTED IN K-TH PATH DETECTION, OR THOSE TO BE DETECTED IN K + 1-TH PATH DETECTION CALCULATED FROM PAIRS OF RECEIVE BEAM NUMBERS AND PATH DELAYS DETECTED UNTIL K-TH DETECTION AND INFORMATION ON USER SIGNAL RECEPTION QUALITY IN PAIRS OF RECEIVE BEAM NUMBERS AND PATH DELAYS



PATH DETECTION RANGE IN K + 1-TH PATH DETECTION (PATH DELAY: WITHIN  $\pm 2$ , RECEIVE BEAM NUMBER: WITHIN  $\pm 1$ , COMBINATION OF RECEIVE BEAM  $\pm 1$  AND PATH DELAY  $\pm 2$  IS EXCLUDED)

Rest Available Copy

# F I G. 4

10/538762

4/4

PATH DELAY (UNIT: CHIP)	RECEIVE BEAM NUMBER (PEAK ANGLE)											
	0	1	2	3	4	5	6	7	8	9	10	11
0.0	(-55)	(-45)	(-35)	(-25)	(-15)	(-05)	(+05)	(+15)	(+25)	(+35)	(+45)	(+55)
0.5												
1.0												
1.5												
2.0												
2.5												
3.0												
3.5												
4.0												
4.5												
5.0												
5.5												
6.0												
6.5												
7.0												
7.5												
8.0												
8.5												
9.0												
9.5												
10.0												
10.5												

PATH DETECTION RANGE SET TO COVER ALL PATH DELAYS AND ALL RECEIVE BEAMS

Rest Available Copy